

Searching for digital signature and analog and signal.

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On-Chip Analog Output Response Compaction - Renovell Azas And (1997) (Correct) (1 citation) been developed in order to obtain a m-bit **Digital Signature** (DS) from an n-vector output response On-Chip **Analog** Output Response Compaction M. Renovell, F. Azas self-test capabilities in **analog** and mixed-**signal** integrated circuits. The integration function is galahad.informatik.tu-chemnitz.de/proceedings/edtc/papers/1997/edt97/htmfiles/sun sgi/.././pdffiles/11c 1.pdf

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